

ID	Task Name	Duration	Start	Finish	Predecessors	Constraint	WBS	Resource Names
878	<b>TRACKING ELECTRONICS FABRICATION</b>	<b>291.4 w</b>	<b>10/3/94</b>	<b>7/28/00</b>		<b>As Soon As</b>		
879	<b>Design</b>	<b>123.4 w</b>	<b>10/3/94</b>	<b>3/7/97</b>		<b>As Soon As</b>		
880	<b>SVXII Rad Hard Chips Available</b>	<b>0 w</b>	<b>10/1/96</b>	<b>10/1/96</b>		<b>Start No Ear</b>		
881	Port Card/VRB Test	4 w	10/3/94	10/28/94		As Soon As f		
882	Port Card Redesign	58 w	10/31/94	12/12/95	881	As Soon As f		EEF115[0.7],ETF115
883	VRB Card Redesign	58 w	10/31/94	12/12/95	881	As Soon As f		EEF115,ETF115
884	Backplane/Download Final Design	58 w	10/31/94	12/12/95	881	As Soon As f		EEF115[0.5],ETF115[0.5]
885	L1/L2 framework specs complete	40 w	8/14/95	5/23/96		Start No Earl		EEU115[0.25]
886	Controller Card Redesign	30 w	5/24/96	1/7/97	881SS+2 w,885	As Soon As f		EEU115
887	<b>M3-Tracking Electronics System Test Begins</b>	<b>0 w</b>	<b>3/7/97</b>	<b>3/7/97</b>	<b>882,883,884,886,635SS</b>	<b>As Soon As</b>		
888	<b>Preproduction</b>	<b>252.4 w</b>	<b>10/3/94</b>	<b>10/11/99</b>		<b>As Soon As</b>		
889	Procure Additional VRB Cards	0.2 w	6/1/99	6/1/99		Start No Earl	1.1.5.3.5(.22)	k\$[0.45]
890	Procure Port Card Parts	16 w	10/2/95	1/25/96		Start No Earl	1.1.5.2.1(.32)	k\$[1.57],k\$c[0.1]
891	Port Card Preproduction	38 w	10/22/97	7/31/98	635SS+20 w	Start No Earl	1.1.5.2.1(.68)	EEF115[0.3],EEU115[0.3],k\$[3.34],k\$c[0.22]
892	<b>Order Placed for Preproduction Port Cards</b>	<b>0 w</b>	<b>10/22/97</b>	<b>10/22/97</b>	<b>891SS</b>	<b>As Soon As</b>		
893	Procure VRB Card Parts	16 w	10/3/94	1/20/95		Start No Earl	1.1.5.2.3(.36)	k\$[2.96],k\$c[0.05]
894	VRB Card Preproduction	17 w	6/15/97	10/14/97	635SS+13 w	Start No Earl	1.1.5.2.3(.55),1.1.5.3.5(.78)	EEF115[0.3],k\$[6.13],k\$c[0.09]
895	<b>Order Placed for VRB Cards</b>	<b>0 w</b>	<b>9/3/97</b>	<b>9/3/97</b>	<b>894SS</b>	<b>As Soon As</b>		
896	Full VRB Crate Test	2 w	7/2/98	7/16/98	894FS+11 w	As Soon As f		
897	Controller Preproduction	73 w	4/23/98	10/11/99	635SS+44 w	As Soon As f		EEF115[0.1],EEU115[0.1]
898	<b>Order Placed for Controller Cards</b>	<b>0 w</b>	<b>10/11/99</b>	<b>10/11/99</b>	<b>897</b>	<b>As Soon As</b>		
899	Preproduction Testing	38 w	10/22/97	7/31/98	891SS	As Soon As f		PhysU115,PhysF115
900	<b>Production</b>	<b>117 w</b>	<b>10/22/97</b>	<b>3/15/00</b>		<b>As Soon As</b>		
901	Port Card Production and Test	79 w	8/3/98	3/15/00	899	As Soon As f		EEF115[0.1],ETU115[2],ETF115[0.3]
902	<b>Final Delivery of Sequencers</b>	<b>0 w</b>	<b>3/15/00</b>	<b>3/15/00</b>	<b>901</b>	<b>As Soon As</b>		
903	VRB Card Production and Test	61.2 w	7/17/98	10/11/99	896	As Soon As f		EEF115,ETU115,ETF115
904	<b>Full Production of Port/VRB Cards Begun</b>	<b>0 w</b>	<b>8/21/98</b>	<b>8/21/98</b>	<b>901SS+3 w,903SS+3 w</b>	<b>As Soon As</b>		
905	Controller Card Production and Test	18 w	10/12/99	3/1/00	897	As Soon As f	1.1.5.2.3(0.09)	EEF115[0.1],ETF115[0.25],k\$[0.74],k\$c[0.01]
906	<b>Full Production of Controller Card Begun</b>	<b>0 w</b>	<b>10/12/99</b>	<b>10/12/99</b>	<b>905SS</b>	<b>As Soon As</b>		
907	Back Plane Production and Test	10 w	10/22/97	1/14/98		Start No Earl		EEF115[0.1]
908	Procure Fiber Optic Cables	12 w	7/15/96	10/7/96		Start No Earl	1.1.5.2.2,1.1.5.3.4	EEF115[0.1],k\$[0.22]
909	Support Services (racks, power supplies, tests, etc. )	131 w	5/1/97	1/4/00		Start No Earl	1.1.1.5.9	EEF115,ETF115
910	System Install and Test	34 w	11/16/99	7/28/00	901FS-4 w,903	As Soon As f		EEF115,PhysU115[2],PhysF115,ETF115[2],MCH1[0.1]
911	<b>Final Electronics Installed</b>	<b>0 w</b>	<b>7/28/00</b>	<b>7/28/00</b>	<b>910</b>	<b>As Soon As</b>		
912	<b>First Readout Crate Installed &amp; Working</b>	<b>0 w</b>	<b>11/16/99</b>	<b>11/16/99</b>	<b>910SS</b>	<b>As Soon As</b>		